

Runtime Verification of C Memory Safety

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Abstract

C is the most widely used imperative system’s implementation language. While C provides types and high-level abstractions, its design goal has been to provide highest performance which often requires low-level access to memory. As a consequence C supports arbitrary pointer arithmetic, casting, and explicit allocation and deallocation. These operations are difficult to use, resulting in programs that often have software bugs like buffer overflows and dangling pointers that cause security vulnerabilities. We say a C program is memory safe, if at runtime it never goes wrong with such a memory access error. Based on standards for writing “good” C code, this paper proposes *strong memory safety* as the least restrictive formal definition of memory safety amenable for runtime verification. We show that although verification of memory safety is in general undecidable, even when restricted to closed, terminating programs, runtime verification of strong memory safety is a decision procedure for this class of programs. We verify strong memory safety of a program by executing the program using a symbolic, deterministic definition of the dynamic semantics. A prototype implementation of these ideas shows the feasibility of this approach.

1 Introduction

Memory safety is a crucial and desirable property for any piece of software. Its absence is a major source for software bugs which can lead to abrupt termination of software execution, but also, and sometimes even more dangerous, can be turned into a malicious tool: most of the recent security vulnerabilities are due to memory safety violations. Nevertheless most existing software applications, and especially performance-critical applications, are written in low-level programming languages such as C, which offer performance at the expense of safety. Due to C’s support of pointer arithmetic, casting, and explicit allocation and deallocation, C program executions can exhibit memory safety violations ranging from buffer overflows, to memory leaks, to dangling pointers.

An important research question is thus the following:

Given a program written in an unsafe programming language like C, how can one guarantee that any execution of this program is memory-safe?

Many different approaches and tools were developed to address this problem. For instance, CCured [7] uses pointer annotations and analyzes the source of the program, trying to prove it memory safe, introducing runtime checks in the code to monitor at runtime the parts which cannot be proven; Purify [4] and Valgrind [8] execute the program in a “debugging” mode, adding metadata to the program pointers to guarantee a proper separation of the allocation zones, and use that metadata to monitor and detect anomalies at runtime; DieHard [1] and Exterminator [9] replace the standard allocation routines by ones using randomization, which enables the detection of errors with high probability, attempting to correct the errors on-the-fly. However, most of these tools arise from ad-hoc observations and practical experience, without formally defining what it means for a program to be memory safe.

This paper makes a first step towards bridging this gap, by introducing a formal definition of memory safety for programs written in a non-memory safe programming language and execution platform. The language and platform chosen to this aim is KERNELC, a formal definition of the executable semantics of a fragment of the C language including memory allocation/freeing routines. KERNELC only supports one type namely mathematical integers; and each KERNELC location can hold exactly one integer. Nevertheless one can write many interesting pieces of C code in KERNELC. Here are some that we will refer to in the paper: ALLOCATE allocates a linked list of 5 nodes, in reversed order, each node having two contiguous locations, one holding a value and the other a pointer to the next node; REVERSE reverses a list of nodes as above that starts at p; and DEALLOCATE frees a list starting with p.

<pre>ALLOCATE n = 0; p = null; while(n != 5) { q = malloc(2); *q = n; *(q+1) = p; p = q; n = n+1; }</pre>	<pre>REVERSE if(p != null) { x = *(p+1); *(p+1) = null; while(x != null) { y = *(x+1); *(x+1) = p; p = x; x = y; } }</pre>	<pre>DEALLOCATE while(p != null) { q = *(p+1); free(p); p = q; }</pre>
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Informally, memory safety means that the program cannot access a memory location which it shouldn't (e.g., exceeding arrays boundaries, addressing unallocated memory, and so on). For example, consider the program ALLOCATE', obtained from ALLOCATE by removing the second statement, i.e., p = null;. Then, any of the composed programs ALLOCATE' DEALLOCATE, or ALLOCATE'

REVERSE, is not memory safe, since the list can potentially be non-null terminated, which would lead to an attempt of accessing non-allocated memory upon deallocating/reversing the list. On the other hand, a compiler might initialize all local variables with 0 (which in C corresponds to `null`); if so our example program would have no memory access error and would terminate.

The principle source of C’s nondeterminism comes from the underspecification of *C’s memory allocator*, which implements the `malloc` and `free` functions. The C language specification guarantees that a call to `malloc(n)` will, if it succeeds, return a pointer to a region of n continuous and previously unallocated locations. These locations now become allocated. When these locations are no longer needed, the pointer, which was returned from a `malloc` call, is passed to `free` which deallocates the memory. The C language specification provides no guarantees except for that fact that `malloc` returns unallocated locations; `free` might deallocate the memory or not. To cope with this non-determinism, memory safety of KERNELC programs is defined as a global property on the entire set of executions of a program, derivable using the KERNELC definition. We say: A KERNELC program is *memory safe* if none of its possible executions gets stuck in a non-final state.

One might expect that verification of memory safety would be decidable for terminating programs – after all we have so many checkers addressing the problem. However, we show that *memory safety is undecidable even for closed, terminating programs*. The argument for undecidability comes from the rather unusual usage of memory allocation, that is, using memory allocation as a source of nondeterminism in the execution, such as the examples in Fig. 1: INPUT presents a simulation of non-deterministic input, and CHOICE shows how one can model nondeterministic choice.

Based on the fact that standards for writing “good” C code [3] advice against taking advantage of this kind of non-determinism, we define *strong memory safety as the least restrictive notion of memory safety amenable for runtime verification*. The runtime verification works as follows:

we introduce SAFEKERNELC, an executable definition for the same language, except that it handles memory allocation symbolically; this makes the memory allocator deterministic; and since SAFEKERNELC is deterministic, there is only one possible execution of any program; therefore, strong memory safety can be monitored along that execution, obtaining a guarantee for memory safety for all possible (partial) executions of the program on the KERNELC operational semantics. Note that strong memory safe programs do not only guarantee memory safety, they also enforce good coding practices and ensure platform portability.

Our contributions are as follows:

- We present KERNELC, a formal definition for the dynamic semantics of

<div style="background-color: #e0e0e0; padding: 2px; text-align: center; font-weight: bold; font-size: small;">INPUT</div> <pre style="margin: 0;">n=malloc(1); while (n!=1) if (n%2) n=3*n+1; else n=n/2;</pre>	<div style="background-color: #e0e0e0; padding: 2px; text-align: center; font-weight: bold; font-size: small;">CHOICE</div> <pre style="margin: 0;">x = malloc(1); y = malloc(1); if (x<y) {} else {}</pre>
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Figure 1: “Accidental” memory safety

a fragment of the C language, and formally define memory safety for an execution, and for a program, in this context. Since our definition is executable, it yields a procedure for runtime verifying memory safety along one possible execution of a program.

- We prove that, even if the input program is closed and known to be terminating, the verification of memory safety is undecidable.
- We refine KERNELC to SAFEKERNELC, and introduce in this context strong memory safety as a meaningful restriction of memory safety.
- We prove that runtime verification of strong memory safety is decidable for closed, terminating programs. Since SAFEKERNELC is also executable, this gives us a sound semi-procedure for checking memory safety of terminating programs.

The remainder of the paper is structured as follows. Section 2 introduces the KERNELC definition and illustrates it through several examples. Section 3 formally defines memory safety, and shows that, although we can monitor memory safety along any execution path, proving memory safety is generally undecidable. Section 4 introduces strong memory safety as a reasonable and decidable restriction of memory safety and shows that runtime verification of strong memory safety yields a sound technique for verifying memory safety. Section 5 concludes.

2 Formal Semantics of KernelC

We here discuss the definition of KERNELC using K [10], a technique for defining languages within the Rewriting Logic Semantics [6, 14]. Within this framework, languages \mathcal{L} are defined as rewrite theories $(\Sigma_{\mathcal{L}}, E_{\mathcal{L}}, R_{\mathcal{L}})$, where $\Sigma_{\mathcal{L}}$ is a signature extending the syntax of \mathcal{L} , $E_{\mathcal{L}}$ is a set of $\Sigma_{\mathcal{L}}$ -equations, which are thought of as structural rearrangements preparing the context for rules and carrying no computational meaning, while $R_{\mathcal{L}}$ is a set of $\Sigma_{\mathcal{L}}$ -rules, used to model irreversible computational steps. Since our base logic, rewriting logic [5], is a conservative extension of equational logic, terms can be replaced by equal terms in any context and in any direction. We write $\mathcal{R} \vdash t = t'$ whenever t can be proved equal to t' using equational deduction with the equations in \mathcal{R} . Like in term rewriting, rules can be applied in any context, but only from left-to-right. One way to think of rewriting logic is that equations apply until the term is *matched* by the left-hand-side (lhs) of some rule, which then irreversibly transforms the term. We write $\mathcal{R} \vdash t \rightarrow t'$ when t can be rewritten, using arbitrarily many equational steps but only one rewrite step in \mathcal{R} , into t' . Also, we write $\mathcal{R} \vdash t \rightarrow^* t'$ when t can be rewritten, using the equations and rules in \mathcal{R} , into t' . Rewriting logic thus captures *rewriting modulo equations* into a logic, with good mathematical properties (loose and initial models, complete deduction, proofs = computations, etc.). It is simple to understand and efficiently executable.

K is a *modular* definitional framework: rules match only what they need from the configuration, so one can change the configuration (e.g., adding store, input/output, stacks, etc.) without having to revisit existing rules.

Sequences, bags and maps. Sequences, bags and maps are core to K language definitions and are defined as standard (equational) data-structures. We use notations $\text{Seq}_u^@[S]$ for sequences and $\text{Bag}_u^@[S]$ for bags, resp., where u is their unit and $_@_$ is their binary construct. Formally, if added for sort S' , these correspond to adding subsorting $S < S'$ (i.e., production $S' ::= S$, not needed when $S' = S$), operations $u : \rightarrow S'$ (a constant) and $_@_ : S' \times S' \rightarrow S'$, and appropriate unit and associativity equations for sequences, and unit, commutativity and associativity equations for bags. For example, an environment is a finite bag of pairs, $\rho[X]$ retrieves the *Int* associated to the *Id* X in ρ , $\rho[X \leftarrow J]$ updates the *Int* corresponding to X in ρ to J , and $\rho \setminus X$ removes pair $X \mapsto _$ from ρ (if there is any). One can also define, in the same style, an operation *Dom* giving the domain of a map as a bag of elements, as well as an operation checking whether the map term is indeed a partial function. These operations are easy to define algebraically and therefore we assume them from here on; in fact, we assume that each map that occurs in an equation or rule is a well-formed map (e.g., the maps $\sigma \otimes \sigma'$ in the rules for `malloc` and `free` in Fig. 2). In general, $\text{Map}_u^@[S_1, S_2]$ corresponds to bags of pairs of elements of sorts S_1 and S_2 , respectively, each pair written $s_1 \mapsto s_2$, with additional operations $_[-] : S' \times S_1 \rightarrow S_2$ and $_[- \leftarrow] : S' \times S_1 \times S_2 \rightarrow S'$ and $_[-] : S' \times S_1 \rightarrow S'$ for lookup, update (adding a new pair if map undefined on that element) and deletion (i.e., removing an element binding), respectively, where S' is the sort corresponding to the maps.

Abstract Syntax. Fig. 2 shows the complete K definition of KERNELC, a C-like language with dynamic memory allocation and deallocation. K definitions typically use only one (abstract) syntactic category, K , serving as minimal syntactic infrastructure to define terms; it is not intended to be used for parsing or type-checking. We make no distinction between algebraic signatures and their context-free notation: syntactic categories correspond to sorts and productions to operations in the signature; for example, production “ $K ::= Id = K$;” is equivalent to defining an operation “ $_ = _ ; : Id \times K \rightarrow K$ ”. In Fig. 2, `op` stands for the various arithmetic and relational operations that one may want to include in one’s language, and op_{Int} stands for the mathematical counterpart (function or relation) of `op` which operates on integers. For example, `op` can range over standard arithmetic operator names `+`, `-`, `*`, `/`, etc., and over standard relational operator names `==`, `!=`, `<=`, `>=`, etc., in which case $+_{Int}$ is the addition operation on integers (e.g., $3 +_{Int} 7 = 10$), etc., and $==_{Int}$ is the equality on integers (e.g., $(3 ==_{Int} 5) = 0$ and $(3 ==_{Int} 3) = 1$). Like in C, we assume that boolean values are special integer values, but, unlike C we assume unbounded integers.

We also assume the C meaning of the language constructs. In particular, `malloc(N)` allocates a block of N contiguous locations and returns a pointer to the first location, and `free(P)` assumes that a block of N locations has been

$Nat ::= \text{naturals}, \quad Int ::= \text{integers}$	(abstract syntax)
$Id ::= \text{identifiers, to be used as variable names}$	
$K ::= Int \mid Id \mid \text{null} \mid *K \mid !K \mid K_1 \text{ op } K_2 \mid K_1 \&\& K_2 \mid K_1 \parallel K_2 \mid$ $\mid K_1=K_2 \mid K; \mid K_1 \ K_2 \mid \{K\} \mid \{\} \mid \text{malloc}(K) \mid \text{free}(K)$ $\mid \text{if}(K_1) \ K_2 \mid \text{if}(K_1) \ K_2 \ \text{else} \ K_3 \mid \text{while}(K_1) \ K_2$	
$\text{null} = 0$	(desugaring of non-core constructs)
$!K = \text{if}(K) \ 0 \ \text{else} \ 1$ $K_1 \&\& K_2 = \text{if}(K_1) \ K_2 \ \text{else} \ 0$ $\{K\} = K$	$K_1 \parallel K_2 = \text{if}(K_1) \ 1 \ \text{else} \ K_2$ $\text{if}(K_1) \ K_2 = \text{if}(K_1) \ K_2 \ \text{else} \ \{\}$
$Cfg ::= \langle \text{Bag}_{-}[CfgItem] \rangle$	(configuration)
$CfgItem ::= \langle K \rangle_k \mid \langle Env \rangle_{env} \mid \langle Mem \rangle_{mem} \mid \langle Ptr \rangle_{ptr}$ $K ::= \dots \mid \text{Seq}_{-}[K]$ $Env ::= \text{Map}_{-}[Id, Int]$	$Mem ::= \text{Map}_{-}^{\otimes}[Nat^+, Int]$ $Ptr ::= \text{Map}_{-}[Nat^+, Nat]$
$*K = (K \rightsquigarrow * \square)$	(computation structural equations)
$K_1 \text{ op } K_2 = (K_1 \rightsquigarrow \square \text{ op } K_2)$ $\text{if}(K_1) \ K_2 \ \text{else} \ K_3 = (K_1 \rightsquigarrow \text{if}(\square) \ K_2 \ \text{else} \ K_3)$ $(K_1=K_2) = (K_2 \rightsquigarrow K_1=\square)$ $\text{malloc}(K) = (K \rightsquigarrow \text{malloc}(\square))$	$I_1 \text{ op } K_2 = (K_2 \rightsquigarrow I_1 \text{ op } \square)$ $K; = K \rightsquigarrow \square;$ $(*K_1=K_2) = (K_1 \rightsquigarrow * \square = K_2)$ $\text{free}(K) = (K \rightsquigarrow \text{free}(\square))$
$\{\} = \cdot$	(semantic equations and rules)
$K_1 \ K_2 = K_1 \rightsquigarrow K_2$ $I_1 \text{ op } I_2 \rightarrow I_1 \text{ op}_{Int} I_2$ $\text{if}(I) \ K_2 \ \text{else} \ K_3 \rightarrow K_3, \text{ where } I = 0$ $\text{if}(I) \ K_2 \ \text{else} \ K_3 \rightarrow K_2, \text{ where } I \neq 0$ $\langle X \rightsquigarrow K \rangle_k \langle X \mapsto I, \rho \rangle_{env} \rightarrow \langle I \rightsquigarrow K \rangle_k \langle X \mapsto I, \rho \rangle_{env}$ $\langle X=I \rightsquigarrow K \rangle_k \langle \rho \rangle_{env} \rightarrow \langle K \rangle_k \langle \rho[X \leftarrow I] \rangle_{env}$ $\langle *P \rightsquigarrow K \rangle_k \langle P \mapsto I \otimes \sigma \rangle_{mem} \rightarrow \langle I \rightsquigarrow K \rangle_k \langle P \mapsto I \otimes \sigma \rangle_{mem}$ $\langle *P=I \rightsquigarrow K \rangle_k \langle P \mapsto I' \otimes \sigma \rangle_{mem} \rightarrow \langle K \rangle_k \langle P \mapsto I \otimes \sigma \rangle_{mem}$ $\langle \text{while}(K_1) \ K_2 \rightsquigarrow K \rangle_k = \langle \text{if}(K_1) \ \{K_2 K_1\} \rightsquigarrow K \rangle_k$ $\langle \text{malloc}(N) \rightsquigarrow K \rangle_k \langle \sigma \rangle_{mem} \langle \pi \rangle_{ptr} \rightarrow \langle P \rightsquigarrow K \rangle_k \langle \sigma \otimes \sigma' \rangle_{mem} \langle \pi[P \leftarrow N] \rangle_{ptr}$ $\text{where } Dom(\sigma') = P, P+N-1$ $\langle \text{free}(P); \rightsquigarrow K \rangle_k \langle \sigma \otimes \sigma' \rangle_{mem} \langle P \mapsto N, \pi \rangle_{ptr} \rightarrow \langle K \rangle_k \langle \sigma \rangle_{mem} \langle \pi \rangle_{ptr}$ $\text{where } Dom(\sigma') = P, P+N-1$	$I; \rightarrow \cdot$

(range of variables: $X \in Id; K, K_1, K_2 \in K; I, I_1, I_2 \in Int; P \in Nat^+; N \in Nat$)

Figure 2: KERNELC in K: Complete Semantics

previously allocated using a corresponding `malloc` and deallocates it.

Definition 1 A KERNELC computation K is **well-formed** iff it is equal (using equational reasoning within KERNELC’s semantics) to a well-formed statement list or expression in C . Also, a computation is **well-terminated** iff it is equal to the unit computation “.” or to an integer value $I \in \text{Int}$.

Syntactic Sugar. The desugaring equations are self-describing; we prefer to desugar derived language constructs wherever possible. The “boolean” constructs `&&` and `||` are shortcut. Even though the conditional is a statement, once all syntactic categories are collapsed into one, K , it can be used to desugar expression constructs as well.

Configurations. We use sequences, bags, maps and abstract syntax as configuration constructors, henceforth just called *cells*.

The configuration of KERNELC is a top $\langle \dots \rangle$ cell containing a “soup” of four sub-cells: a cell $\langle \dots \rangle_k$ wrapping the computation; a cell $\langle \dots \rangle_{env}$ holding the mapping for the stack variables; a cell $\langle \dots \rangle_{mem}$ holding the memory (or heap) which can be dynamically allocated/deallocated; and a cell $\langle \dots \rangle_{ptr}$ associating to pointers returned by `malloc` the number of locations that have been allocated (this info is necessary for the semantics of `free`).

K definitions achieve context-sensitivity in two ways: (1) by adding algebraic structure to configurations and using it to control matching; and (2) by extending the original language syntax with a special task sequentialization construct, “ \curvearrowright ” pronounced “then”, as well as frozen variants of existing language constructs. Frozen operators have a “ \square ” as part of their name and are used to “freeze” fragments of program until their turn comes.

Definition 2 Let (Σ, E) be the algebraic specification of KERNELC configurations: Σ contains all the configuration constructs (for bags, maps, etc.) and E contains all their defining equations (associativities, commutativities, etc.). Let \mathcal{T} be the Σ -algebra of ground terms; the E -equational classes (i.e., provably equal using equational reasoning with E) of (ground) terms in \mathcal{T} of sort Cfg which have the form $\langle \langle K \rangle_k \langle \rho \rangle_{env} \langle \sigma \rangle_{mem} \langle \pi \rangle_{ptr} \rangle$ are called (**concrete**) **configurations**. We distinguish several types of configurations:

- Configurations of the form $\langle \langle K \rangle_k \langle \cdot \rangle_{env} \langle \cdot \rangle_{mem} \langle \cdot \rangle_{ptr} \rangle$ where K is a well-formed computation, also written more compactly $\llbracket K \rrbracket$, are called **initial configurations**;
- Configurations $\langle \langle K \rangle_k \langle \rho \rangle_{env} \langle \sigma \rangle_{mem} \langle \pi \rangle_{ptr} \rangle$ whose embedded computation K is well-terminated (a “.” or an $I \in \text{Int}$) are called **final configurations**;
- Configurations $\gamma \in \mathcal{T}$ which cannot be rewritten anymore (i.e., there is no configuration $\gamma' \in \mathcal{T}$ such that $\text{KERNELC} \vdash \gamma \rightarrow \gamma'$) are **normal form configurations**;

- Normal form configurations which are not final are called **stuck** (or **junk**, or **core dump**) configurations;
- Configurations γ which cannot be rewritten infinitely (i.e., there is no infinite set of configurations $\{\gamma_n\}_{n \in \text{Nat}}$ such that $\gamma_0 = \gamma$ and $\text{KERNELC} \vdash \gamma_n \rightarrow \gamma_{n+1}$ for any $n \in \text{Nat}$) are called **terminating configurations**.

Computation structures. Sort K contains computation structures, or simply *computations*, obtained by adding to the original abstract syntax *computation sequences* (terms in $\text{Seq} \cdot \sim [K]$) and *frozen computations* (wrapped by operators containing a “ \square ” in their name). Intuitively, $K_1 \sim K_2$ means “first process K_1 , then process K_2 ”. Frozen computations are structurally inhibited from advancing until their turn comes. For example, “ $K_1 \text{ op } K_2$ ” first processes K_1 and in the meanwhile keeps K_2 frozen: “ $K_1 \text{ op } K_2 = K_1 \sim \square \text{ op } K_2$ ”. After K_1 is processed, its result is placed back in context and K_2 is “scheduled”: “ $I_1 \text{ op } K_2 = K_2 \sim I_1 \text{ op } \square$ ”. As equations, these can be applied forth (to “schedule” for processing) and back (to “plug” results back). We assume all freezing operators are automatically added to sort K (i.e., the “...” in “ $K ::= \dots$ ” in Fig. 2 include “ $Id = \square;$ ” and “ $\square \text{ op } K | K \text{ op } \square$ ” for all operations op that one in the language). Computation equations give the evaluation strategy of each language construct; note the one for the conditional, which schedules for processing the condition, keeping the two branches frozen. They accomplish the same role as the context productions of evaluation contexts [16], but logically rather than syntactically.

Semantic equations and rules. Empty blocks and sequential composition are dissolved into the unit and the sequentialization of K . The rules for $+$, $==$ and **if** are clear. The rule for variable assignment updates the environment, at the same time dissolving the assignment statement. We chose to let lookup of uninitialized variables be undefined. Pointer lookup and update are similar, replacing the environment by memory.

The equation of **while** shows a use of the cell structure to achieve context sensitivity; if replacing it with the simple-minded equation (or rule in case one prefers to regard loops unrolling as a computational step) $\text{while}(K_1)K_2 = \text{if}(K_1)\{K_2; \text{while}(K_1)K_2\}$ then there is nothing to prevent the application of this equation again on the **while** term inside the conditional, and so on. While proof-theoretically one could argue that there is no problem with that, operationally it is problematic as it leads to operational non-termination even though the program may terminate. Therefore, we chose to restrict the unrolling of **while** to only the cases when **while** is the first computation task.

The rules for **free** and **malloc** make subtle use of matching modulo associativity and commutativity of \otimes . In the case of $\text{free}(P)$, a σ' is matched in the $\langle \dots \rangle_{\text{mem}}$ cell whose domain is the N contiguous locations $P, P+N-1$, where N is the natural number associated to P in the $\langle \dots \rangle_{\text{ptr}}$ cell (i.e., the number of locations previously allocated at P using a **malloc**); then the **free** statement in cell $\langle \dots \rangle_k$, the memory map σ' in cell $\langle \dots \rangle_{\text{mem}}$ and the pointer mapping $P \mapsto N$ in cell $\langle \dots \rangle_{\text{ptr}}$ are discarded; this way, the memory starting with location P can be reclaimed

and reused in possible implementations of `KERNELC`. Recall that we assume that all (partial) maps appearing in any context are well-formed; in particular, the map $\sigma \otimes \sigma'$ in the rule of `free` is well-formed, which means that there is only one such matching in the memory cell (P and N are given), which means that the rule for `free` is deterministic. Such a compact and elegant definition is possible only thanks to the strength of matching and rewriting modulo equations. Maude [2] provides efficient support for these operations, which is what makes it a very convenient execution vehicle for K . The well-formedness of maps can either be assumed (one can prove aside that each equation/rule preserves it) or checked as a condition attached to the rule. Fig. 3 shows a rewriting logic derivation using the K semantics in Fig. 2; \rightarrow^* stands for one or more rewrite steps, with arbitrarily many equational steps in between.

Let `REVERSE` be the list reverse program in Introduction, and let

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WHILE  $\equiv$  while(x!=null){y=*(x+1);*(x+1)=p;p=x;x=y;}
IF  $\equiv$  if( $\square$ ){x=*(p+1); *(p+1)=null; WHILE};

```

Also, let us assume the environment and memory maps: ($\rho_1 \equiv p \mapsto 1, x \mapsto 0, y \mapsto 0$), ($\rho_2 \equiv p \mapsto 1, x \mapsto 5, y \mapsto 0$), ($\rho_3 \equiv p \mapsto 5, x \mapsto 0, y \mapsto 0$), ($\sigma_1 \equiv 1 \mapsto 7 \otimes 2 \mapsto 5 \otimes 5 \mapsto 9 \otimes 6 \mapsto 0$), ($\sigma_2 \equiv 1 \mapsto 7 \otimes 2 \mapsto 0 \otimes 5 \mapsto 9 \otimes 6 \mapsto 0$), ($\sigma_3 \equiv 1 \mapsto 7 \otimes 2 \mapsto 0 \otimes 5 \mapsto 9 \otimes 6 \mapsto 1$). The following derivation shows an execution reversing a list with the elements 7, 9:

$\langle \text{REVERSE}(p) \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} = \langle p \neq \text{null} \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$	$\langle ! (p = \text{null}) \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \equiv^*$
$\langle \text{if } (p = 0) \ 0 \ \text{else } 1 \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$
$\langle p = 0 \sim \text{if } (\square) \ 0 \ \text{else } 1 \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$
$\langle p \sim \square = 0 \sim \text{if } (\square) \ 0 \ \text{else } 1 \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow$
$\langle 1 \sim \square = 0 \sim \text{if } (\square) \ 0 \ \text{else } 1 \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$
$\langle 1 = 0 \sim \text{if } (\square) \ 0 \ \text{else } 1 \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow$
$\langle 0 \sim \text{if } (\square) \ 0 \ \text{else } 1 \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} =$
$\langle \text{if } (0) \ 0 \ \text{else } 1 \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow \langle 1 \sim \text{IF} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$
$\langle x = *(p+1); \sim *(p+1) = 0; \sim \text{WHILE} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$
$\langle x = *2; \sim *(p+1) = 0; \sim \text{WHILE} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$
$\langle x = 5; \sim *(p+1) = 0; \sim \text{WHILE} \rangle_k \langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$	$\langle \rho_1 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$
$\langle *(p+1) = 0; \sim \text{WHILE} \rangle_k \langle \rho_2 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$	$\langle \rho_2 \rangle_{env} \langle \sigma_1 \rangle_{mem} \rightarrow^*$
$\langle \text{if } (x \neq 0) \ \{y = *(x+1); *(x+1) = p; p = x; x = y; \text{WHILE}\} \rangle_k \langle \rho_2 \rangle_{env} \langle \sigma_2 \rangle_{mem} \rightarrow^*$	$\langle \rho_2 \rangle_{env} \langle \sigma_2 \rangle_{mem} \rightarrow^*$
$\langle y = *(x+1); \sim *(x+1) = p; p = x; x = y; \text{WHILE} \rangle_k \langle \rho_2 \rangle_{env} \langle \sigma_2 \rangle_{mem} \rightarrow^*$	$\langle \rho_2 \rangle_{env} \langle \sigma_2 \rangle_{mem} \rightarrow^*$
$\langle *(x+1) = p; \sim p = x; \sim x = y; \sim \text{WHILE} \rangle_k \langle \rho_2 \rangle_{env} \langle \sigma_2 \rangle_{mem} \rightarrow^*$	$\langle \rho_2 \rangle_{env} \langle \sigma_2 \rangle_{mem} \rightarrow^*$
$\langle p = x; \sim x = y; \sim \text{WHILE} \rangle_k \langle \rho_2 \rangle_{env} \langle \sigma_3 \rangle_{mem} \rightarrow^*$	$\langle \rho_2 \rangle_{env} \langle \sigma_3 \rangle_{mem} \rightarrow^*$
$\langle \text{if } (x \neq 0) \ \{y = *(x+1); *(x+1) = p; p = x; x = y; \text{WHILE}\} \rangle_k \langle \rho_3 \rangle_{env} \langle \sigma_3 \rangle_{mem} \rightarrow^*$	$\langle \rho_3 \rangle_{env} \langle \sigma_3 \rangle_{mem} \rightarrow^*$
$\langle \cdot \rangle_k \langle \rho_3 \rangle_{env} \langle \sigma_3 \rangle_{mem}$	

Figure 3: Rewriting logic derivation using the `KERNELC` semantics in Fig. 2.

The most intricate rule in Fig. 2 is that of `malloc` which is an almost exact dual of the rule for `free`. Like in the `free` rule, the σ' is doubly constrained: its domain is disjoint from σ 's (because $\sigma \otimes \sigma'$ is well-formed) and its domain

is the set of contiguous locations $\overline{P, P+N-1}$ with P the returned pointer. However, the constraints on σ' are loose enough to allow a high degree of semantic non-determinism. E.g., program “BAD \equiv `p=malloc(2);*2=7;`” may exhibit three different types of behavior, two in which it terminates normally but in non-isomorphic configurations, and one in which it gets stuck looking up for location 1 which is not allocated. E.g., $\langle\langle\text{BAD}\rangle_k\rangle_{env}\langle\cdot\rangle_{mem}\langle\cdot\rangle_{ptr}$ rewrites to any of the following (each being a normal form):

$$\begin{aligned} &\langle\langle\cdot\rangle_k\rangle_{env}\langle p \mapsto 1\rangle_{mem}\langle(1 \mapsto i) \otimes (2 \mapsto 7)\rangle_{mem}\langle 1 \mapsto 2\rangle_{ptr} \text{ where } i \in Int \\ &\langle\langle\cdot\rangle_k\rangle_{env}\langle p \mapsto 2\rangle_{mem}\langle(2 \mapsto 7) \otimes (3 \mapsto j)\rangle_{mem}\langle 2 \mapsto 2\rangle_{ptr} \text{ where } j \in Int \\ &\langle\langle *2 \sim \square=7;\rangle_k\rangle_{env}\langle p \mapsto 5\rangle_{mem}\langle(5 \mapsto k) \otimes (6 \mapsto -3)\rangle_{mem}\langle 5 \mapsto 2\rangle_{ptr} \text{ where } k \in Int \end{aligned}$$

In concrete implementations of KERNELC, one may see the last type of behavior more frequently than the other two, as it is little likely that `malloc` allocates at the “predicted” location, 2 in our case. We tried this code in `gcc` on a Linux machine (casting 2 to `(T*)2`) and it compiled (but it gave an expected segmentation fault when run). Thus, we can regard the third normal form term above as a “core dump”.

We claim that, in spite of this apparently undesired non-determinism, this is the most general semantics of `malloc` that a language designer may want to have. Any other additional constraints, such as “always allocate a fresh memory region”, or “always reuse existing memory if possible”, etc., may lead to a restrictive definition of KERNELC, possibly undesired by some implementors. The actual C language makes no specific requirements on memory allocation, allowing C interpreters or compilers freedom to choose among various memory allocation possibilities; it is programmers’ responsibility to write programs that do not rely on particular memory allocation strategies. Note that, for simplicity, our semantics abstracts from the fact that `malloc` can fail, in which case a null pointer is returned; that is similar to saying we assume an unbounded memory.

The language. We can now formally state what KERNELC is:

Definition 3 *The language KERNELC discussed here is the rewrite logic theory $(\Sigma_{\text{KERNELC}}, E_{\text{KERNELC}}, R_{\text{KERNELC}})$ depicted in Fig. 2. If $\text{KERNELC} \vdash \gamma \rightarrow^* \gamma'$ we say that, in KERNELC, configuration γ **rewrites to** configuration γ' .*

Both the abstract syntax of KERNELC and Σ are included in Σ_{KERNELC} , and also both the desugaring equations of derived KERNELC constructs and E are included in E_{KERNELC} ; recall from Definition 2 that (Σ, E) is the equational definition of KERNELC configurations.

Therefore, the rewrite logic semantics of KERNELC, identified with KERNELC from here on, can produce by means of rewriting all the possible complete or intermediate executions that the language can yield. In particular, if $\text{KERNELC} \vdash \llbracket K \rrbracket \rightarrow^* \gamma$ with K a well-formed computation and γ a well-terminated configuration, then γ contains the (possibly non-deterministic) “result” obtained after “evaluating” K . In addition to comprising all the good executions, the rewrite theory KERNELC also comprises all the bad executions of KERNELC programs,

namely all those that can get stuck; as seen shortly, this is very important as it will allow us to formally define memory safety of KERNELC programs.

Note that like in any other formal operational semantics, our rewrite logic definition of KERNELC has the property that informal execution steps and whole executions of programs become, respectively, formal proof steps and whole proofs in rewriting logic. Interestingly, unlike in other operational semantic frameworks, rewriting logic also provides models which are complete for its proof system, so the very same K definition of KERNELC is also a loose “denotational” semantics in addition to being an “operational” one; moreover, since rewriting logic admits initial models, which are essentially built as a fix point over the algebra of terms, there is a selected subset of models, the “reachable” ones, for which induction is valid. In other words, once one has a K definition of a language, one needs no other formal semantics of that language because its K definition already provides everything one may need from a formal semantics. This is also one of the reasons for which we call K semantics *executable* rather than operational; the latter may give the wrong impression that the K semantics can only be used to yield an interpreter for the language.

Even though K is executable by its very nature, here we actually *defined*, and not *implemented*, KERNELC. We therefore wanted to keep our semantics as loose, or unconstrained, as possible. As usual, when implementing non-deterministic specifications one needs not (and typically does not) provide all the non-deterministic behaviors in one’s implementation. In fact, each implementation of KERNELC is expected to be deterministic. The non-determinism of `malloc` in our KERNELC definition is a result of a deliberate language *under-specification*, not a desired non-deterministic feature of the language. General details on under-specification versus non-determinism are beyond our scope here, but the interested reader is referred to [15] for an in-depth discussion on these subjects. An additional advantage of the under-specified `malloc` in our definition of KERNELC is that it allows us to elegantly yet rigorously define memory safety in the next section: a program is memory-safe iff it cannot get stuck, i.e., it cannot be rewritten to a normal form whose computation cell is not well-terminated.

3 Memory Safety

We here give a formal definition to *memory safety* in KERNELC, capturing the intuition that a program is memory safe iff it is so under any possible implementation of KERNELC, i.e., under any possible choice the rule for `malloc` may make. Due to the undecidability of termination in general, our notion of memory safety, like any other practical (i.e., not unreasonably restricted) notion of memory safety, is undecidable in general. In this section we show that memory safety is actually undecidable even for terminating KERNELC programs. That means, in particular, that KERNELC semantics as well as any faithful implementation of it, cannot detect memory safety violations even on programs which always terminate, no matter whether that is attempted statically or at

runtime.

To check memory safety, one therefore needs either to rely on user help (e.g., annotations), as detailed in [12], or to restrict the class of memory safe programs, which is what we do in next section.

Definition 4 *Well-formed computation K is **terminating** iff $\llbracket K \rrbracket$ is a terminating configuration in KERNELC, and is **memory safe** iff any normal form of $\llbracket K \rrbracket$ in KERNELC is final.*

Program “BAD \equiv p=malloc(2);*2=7;” is terminating but not memory safe: \llbracket BAD \rrbracket rewrites, as seen, to normal form $\langle\langle *2 \sim \square = 7; \rangle_k \langle p \mapsto 5 \rangle_{env} \langle (5 \mapsto -1) \otimes (6 \mapsto -3) \rangle_{mem} \langle 5 \mapsto 2 \rangle_{ptr} \rangle$. Program “GOOD \equiv p=malloc(2);*(p+1)=7;”, on the other hand, is both terminating and memory-safe: \llbracket GOOD \rrbracket rewrites only to normal form configurations of the form $\langle\langle \cdot \rangle_k \langle p \mapsto i \rangle_{env} \langle (i \mapsto j) \otimes (i+1 \mapsto 7) \rangle_{mem} \langle i \mapsto 2 \rangle_{ptr} \rangle$, where $i \in Nat^+$ and $j \in Int$. Program “p=malloc(1);while(*p){}” is memory safe but not terminating (when $*p \neq 0$), and finally, program “p=malloc(1);while(*1){}” is neither memory-safe (when $p \neq 1$) nor terminating (when $p = 1$ and $*1 \neq 0$).

For our simple language, memory is the only source of unsafety; for more complex languages, one may have various types of safety, depending upon the language construct at the top of the computation in t when t is a normal form, which tells why the computation got stuck; e.g., if the language has division and $3/0$ is at the top of the computation, then K got stuck because a division by zero was attempted.

KERNELC is Turing complete (we assumed both arbitrarily large integers and infinite memory), so termination of KERNELC programs is undecidable. That immediately implies that memory safety is also undecidable in general: for any memory safe program PGM, the program “PGM;BAD” is memory safe iff PGM does not terminate. What is not so obvious is the decidability or undecidability of memory safety on terminating programs. In the remaining of this section we show that this is actually an undecidable problem.

A hasty reader may think that, since programs have no symbolic inputs or data, memory safety must be decidable on terminating programs: one can simply run the program and check each memory access. The complexity of the problem comes from the non-determinism/under-specification of malloc, which makes any particular execution of the program to mean close to nothing wrt memory safety. Consider, for example, an execution of the program “x=malloc(1); free(x); y=malloc(1); *x=1;” in which the second malloc *just happens* to return the same pointer as the first malloc. Since this particular execution taking place on a hypothetical particular implementation of KERNELC terminates normally, one may be wrongly tempted to say that it is memory safe; this program is clearly *not* memory safe (gets stuck if second malloc chooses a different location) and even the execution itself can be argued as memory unsafe, because of a memory leak on x (dangling pointer).

Proposition 5 *Memory safety of terminating KERNELC programs is an undecidable property.*

Proof. Since KERNELC is Turing complete, we can encode any decidable property $\varphi(n)$ of input $n \in \text{Nat}$ as a *terminating* and *memory-safe* KERNELC program “ $\mathbf{x}=n; \text{PGM}_\varphi$ ” which writes some variable out , such that $\varphi(n)$ holds iff $\text{KERNELC} \vdash \llbracket \mathbf{x}=n; \text{PGM}_\varphi \rrbracket \rightarrow^* \langle \langle \cdot \rangle_k \langle \text{out} \mapsto 1, \dots \rangle_{\text{env}} \dots \rangle$ and $\varphi(n)$ does not hold iff $\text{KERNELC} \vdash \llbracket \mathbf{x}=n; \text{PGM}_\varphi \rrbracket \rightarrow^* \langle \langle \cdot \rangle_k \langle \text{out} \mapsto 0, \dots \rangle_{\text{env}} \dots \rangle$. Since the pointer returned by `malloc` is non-deterministic, we can use it to “choose a random” n to assign to \mathbf{x} : consider the program “ $\text{PGM}'_\varphi \equiv \mathbf{x}=\text{malloc}(1); \text{PGM}_\varphi; \text{if}(\text{out})\text{GOOD} \text{ else BAD}$ ”. PGM'_φ terminates because “ $\mathbf{x}=n; \text{PGM}_\varphi$ ” terminates for any $n \in \text{Nat}$ returned by `malloc(1)` and the conditional always terminates. On the other hand, PGM'_φ is memory safe iff the variable out is 1 in the environment when PGM_φ terminates, which happens iff $\varphi(n)$ holds for all $n \in \text{Nat}$. The undecidability of memory safety then follows from the fact that there are decidable properties φ for which $(\forall n)\varphi(n)$ is a proper co-recursively-enumerable property [11]. \square

Since our notion of memory safety refers to a program rather than a path, the proposition above says that it is also impossible to devise any runtime checker for memory safety of general purpose KERNELC (and hence C) programs. One could admittedly argue that such anomalies occur as artifacts of poorly designed languages like C, that allow for (too) direct memory access and complete freedom in handling pointers as if they are natural numbers. However, it is actually precisely these capabilities that make C attractive when performance is a concern, and performance is indeed a concern in many applications. That memory unsafe programs may execute just fine is a *must* feature of any formal semantic definition of C that is worth its salt, because all C implementations deliberately “suffer” from this problem.

Since unrestricted use of pointers returned by `malloc` can lead to non-deterministic executions of programs, one could, in principle, introduce some notion of “path memory safety”. For example, one could argue that an execution of the program “ $\mathbf{x}=\text{malloc}(1); \mathbf{y}=\text{malloc}(1); \text{if}(\mathbf{y}==\mathbf{x}+1) \{\} \text{ else BAD}$ ” in which \mathbf{y} just happens to be $\mathbf{x}+1$ is memory safe, or that an execution of the program “ $\mathbf{x}=\text{malloc}(2); \text{if}(\mathbf{*x}==\mathbf{*(x+1)}) \{\} \text{ else BAD}$ ” in which $\mathbf{*x}$ just happens to be $\mathbf{*(x+1)}$ is memory safe. Encouraged by the informal so-called “C rules for pointer operations” [3], we prefer to not introduce such a notion of “path memory safety” and, instead, to keep our notion of memory safety of programs in Definition 4; with it, these terminating programs are not memory safe. We will next introduce a stronger notion of memory safety, supported by an executable semantics that will always get stuck on these programs.

4 Strong Memory Safety

We propose the semantic notion of *strong memory safety*: a program is strongly memory safe iff it does not get stuck in the executable semantics `SAFEKERNELC`, a variant of KERNELC semantics with symbolic pointers. Interestingly, our formal definition of strong memory safety includes the informal notion of memory safety implied by the “C rules for pointer operations” [3]. Strong memory safety is shown decidable for terminating programs, but, of course, it is undecidable

in general.

Note that we are not attempting to fix C's problems here, nor to propose a better language design. However, the high degree of non-determinism in the semantics of `malloc` may be problematic in formal verification. We prefer to give a slightly different semantics to our language, one which captures the non-determinism of `malloc` *symbolically*. Fig. 4 shows the formal K semantic definition of `SAFEKERNELC`, which essentially adds symbolic numbers and gives `malloc` a symbolic semantics. Everything else stays unchanged, like in the definition of `KERNELC` in Fig. 2.

The first distinction between `KERNELC` and `SAFEKERNELC` is that, although both of them are deterministic for all rules except the `malloc` rule, `KERNELC` can introduce non-determinism based on the values returned by the `malloc` function, while `SAFEKERNELC`, using symbolic values, is deterministic up to symbolic variable renaming.

Proposition 6 *For any $\mathcal{L} \in \{\text{KERNELC}, \text{SAFEKERNELC}\}$, if $\mathcal{L} \vdash \gamma_0 \rightarrow^* \gamma$ such that $\mathcal{L} \vdash \gamma = \langle\langle K \rangle_k \langle \rho \rangle_{env} \langle \sigma \rangle_{mem} \langle \pi \rangle_{ptr}\rangle$, and $\mathcal{L} \not\vdash K = \text{malloc}(N); \rightsquigarrow K'$, then there exists at most one configuration γ' such that $\mathcal{L} \vdash \gamma \rightarrow \gamma'$. `SAFEKERNELC` is deterministic, modulo renamings of the symbols from `NatVar`.*

Proof. First part can be formally proved by induction on the length of the derivation. Intuitively, the property holds because, at any moment, there exists a unique way to match a configuration which is reachable from an initial state, and the rules preserve this invariant. Moreover, except for the `malloc` rule, which allows for a choice of the value introduced (but does not violate the invariant), all other rules have the variables in the right hand side completely determined by those in the left hand side. For the second part, since the value introduced by the `malloc` rule is only operated with symbolically, which corresponds to the fact that future side conditions must hold for all possible valuations of variable, it follows that we can choose a canonical way to generate fresh symbols and thus completely eliminate the non-determinism. \square

Therefore, we can choose a representative derivation for any `SAFEKERNELC` derivation of an initial state, say one in which choosing of fresh variables is done in order from the countable infinite sequence $nv_1, nv_2, \dots, nv_N, \dots$

Definition 7 *Well-formed computation K is **strongly terminating** iff $\llbracket K \rrbracket$ is terminating in `SAFEKERNELC`, and is **strongly memory safe** iff any normal form of $\llbracket K \rrbracket$ in `SAFEKERNELC` is final.*

Since `SAFEKERNELC` adds symbolic values (for pointers and initial values in allocated memory locations), the assumed machinery for naturals and integers is now expected to work with these symbolic values as well. In particular, the rule (side) conditions may be harder to check. For example, the rule " $I_1 == I_2 \rightarrow N$ " applies only when one proves that $I_1 = I_2$, and in that case N is 1, or when one proves that $I_1 \neq I_2$, and in that case N is 0; if one cannot prove any of the two, then the term " $I_1 == I_2$ " remains unreduced and

$NatVar ::=$ infinite set of symbolic natural numbers	(abstract syntax)
$Nat ::= \dots \mid NatVar$	
(semantic equations and rules)	
$\langle \text{malloc}(N) \rightsquigarrow K \rangle_k \langle \sigma \rangle_{mem} \langle \pi \rangle_{ptr} \rightarrow \langle p \rightsquigarrow K \rangle_k \langle \sigma \otimes \sigma' \rangle_{mem} \langle \pi[P \leftarrow N] \rangle_{ptr}$	
where P is a fresh symbol in $NatVar$ and $Dom(\sigma') = P, P + N - 1$	

Figure 4: Formal semantics of SAFEKERNELC.
 (figure only shows how it differs from the semantics of KERNELC in Fig. 2)

the execution of the program may get stuck because of that. For example, both “ $p = \text{malloc}(1); \text{while}(*p) \{ \}$ ” and “ $p = \text{malloc}(1); \text{while}(*1) \{ \}$ ” are now strongly terminating (but remain memory unsafe, also in the strong sense). Also, both programs discussed in front of Proposition 5 get stuck when processing the conditions of their if statements. On the positive side, programs obeying the recommended safety rules for pointer operations in C [3], e.g., reading only initialized locations and comparing pointers only if they are within the same data-structure contiguously allocated in memory, are strongly memory safe. For example, “ $n = 100; a = \text{malloc}(n); x = a; \text{while}(x \neq a + n) \{ *x = 0; x = x + 1; \}$ ” is both strongly memory safe and strongly terminating.

Since the side conditions can get arbitrarily complicated (they depend on the program), the problem of deciding their validity itself can potentially become undecidable. Therefore, we will assume an oracle for the logic involving the side conditions, which, given a formula, can give one of the following answers: YES, if the formula holds, NO, if it does not hold, or MAYBE, if the oracle cannot decide the problem. For example, this oracle could be a sound automatic theorem prover, which will attempt to prove/disprove the theorem, but, being incomplete, might also fail on complex formulas. Since the logic (regarding pointer comparison) involved in programs following “good coding standards” should be relatively simple, e.g., Presburger arithmetics, we believe the oracle will probably act as a decision procedure for the majority of code. Matching logic [12] shows how one could use the same framework (together with code annotations), to prove general purpose properties about programs, which would allow checking memory safety for “bad” coded programs, as well. The advantage of the technique presented here is that it is fully automatic and requires no additional user input.

Proposition 8 *Let $p \in K$ be a program. Then*

1. *If p is terminating then p is strongly terminating;*
2. *If p is strongly memory safe then p is memory safe;*
3. *If p is strongly memory safe then p is terminating iff p is strongly terminating.*

Proof. 1. We will show that, any execution of p in SAFEKERNELC, can be step-by-step simulated in KERNELC, therefore, any non-terminating execution in SAFEKERNELC would have a corresponding non-terminating execution in

KERNELC. Suppose $(\gamma_i)_{i \geq 0}$ is a sequence of configurations satisfying that γ_0 is an initial configuration corresponding to p , and, for any i , $\text{SAFEKERNELC} \vdash \gamma_i \xrightarrow{\theta_{i+1}(\rho_{i+1})} \gamma_{i+1}$. Also suppose this derivation sequence is representative, in the sense that fresh *NatVar* symbols are generated (in order) from the sequence $(nv_j)_{j \geq 1}$. Let $(i_j)_{j \geq 1}$ be a sequence of positive numbers such that ρ_{i_j} is the instance of the **malloc** rule introducing nv_j . It follows that, for any $i \leq i_{j_0}$, γ_i is completely determined by $(nv_j)_{1 \leq j \leq j_0}$; that is, all values in the environment and store are algebraic expressions with variables from $(nv_j)_{1 \leq j \leq j_0}$. We define the following sequence of functions $V_j : \{nv_1, \dots, nv_j\} \rightarrow \text{Nat}$, by: $V_1(nv_1) = 1$, and $V_{n+1}(nv_j) = \begin{cases} V_n(nv_j), j \leq n \\ 1 + V_n(nv_n) + \bar{V}_n(N), j = n + 1 \end{cases}$, where N is the number associated to nv_n in γ_{i_n} , and \bar{V}_n is the canonical extension of V_n to expressions and configurations (mapping $\sigma(nv_j)$ to 0). Let V be the limit of $(V_j)_{j \geq 1}$, i.e., $V(nv_j) = V_j(nv_j)$. It remains to show that, for any $i \geq 0$, $\bar{V}(\gamma_i)$ is a configuration for KERNELC, and $\text{KERNELC} \vdash \bar{V}(\gamma_i) \xrightarrow{\bar{V}(\theta_{i+1}(\rho_{i+1}))} \bar{V}(\gamma_{i+1})$ (whence $\bar{V}(\theta_{i+1}(\rho_{i+1}))$ is an instance of a KERNELC rule). For $i = 0$, $\bar{V}(\gamma_0) = \gamma_0$, since γ_0 does not contain any *NatVar* symbols, whence it also is a configuration for KERNELC. Now, suppose $\bar{V}(\gamma_i)$ is a KERNELC configuration, and that $\text{SAFEKERNELC} \vdash \gamma_i \xrightarrow{\theta_{i+1}(\rho_{i+1})} \gamma_{i+1}$. If ρ_{i+1} is not the **malloc** rule, then it basically is the same rule schema as in KERNELC, with the difference that it can be instantiated for terms with symbols from *NatVar*. Since the *NatVar* symbols are free variables in the instance $\theta_{i+1}(\rho_{i+1})$, it follows that $\bar{V}(\theta_{i+1}(\rho_{i+1}))$ is an instance for both KERNELC and SAFEKERNELC; therefore it must be that $\text{KERNELC} \vdash \bar{V}(\gamma_i) \xrightarrow{\bar{V}(\theta_{i+1}(\rho_{i+1}))} \bar{V}(\gamma_{i+1})$. Similarly, if $\theta_{i+1}(\rho_{i+1})$ is an instance of the **malloc** rule, then $\bar{V}(\theta_{i+1}(\rho_{i+1}))$ is an instance of the **malloc** rule in KERNELC (by the construction of V).

2 and 3. Let γ_0 be an initial state and let $\text{SAFEKERNELC} \vdash \gamma_i \xrightarrow{\theta_{i+1}(\rho_{i+1})} \gamma_{i+1}$, $0 \leq i < \bar{n}$, where $\bar{n} \in \text{Nat} \cup \{\infty\}$, be the (possibly infinite) canonical derivation of γ_0 in SAFEKERNELC. Assume γ_0 corresponds to a strongly safe program. This means that if \bar{n} is finite, then $\gamma_{\bar{n}}$ is final. We will show that all (complete) derivations of γ_0 in KERNELC are valuations of the canonical derivation of γ_0 in SAFEKERNELC; therefore, they either are non-terminating, or they terminate in a final configuration. Let $\text{KERNELC} \vdash \gamma'_i \xrightarrow{\theta'_{i+1}(\rho'_{i+1})} \gamma'_{i+1}$, $0 \leq i < \bar{n}'$ be a derivation such that $\gamma'_0 = \gamma_0$. Let $(i_j)_{j \geq 1}$ be the increasing sequence of positive numbers such that ρ'_{i_j} is an instance of the **malloc** rule. We then let $V(nv_j) = \theta'_{i_j}(P)$. It can be proved by induction on i that $\bar{V}(\theta_i(\rho_i)) = \theta'_i(\rho_i)$, and therefore $\bar{V}(\gamma_i) = \gamma_i$. Moreover, if $\bar{n}' < \bar{n}$, then, using a construction similar to the one used in proving the claim above, we expand the existing valuation V to one covering all symbols in the SAFEKERNELC derivation, say V' , and use that to expand the existing KERNELC derivation to $\text{KERNELC} \vdash \bar{V}'(\gamma_i) \xrightarrow{\bar{V}'(\theta_{i+1}(\rho_{i+1}))} \bar{V}'(\gamma_{i+1})$, $\bar{n}' \leq i < \bar{n}$, such that $\bar{V}'(\gamma_{\bar{n}'}') = \gamma'_{\bar{n}'}$. \square

The first two implications in the proposition above are proper. For example,

“`p=malloc(1);while(*p){}`” is a strongly terminating program (but not strongly memory safe) which is not terminating. We call such programs “accidentally non-terminating”. There are also programs which are memory safe but not strongly memory safe, such as “`x=malloc(1);y=malloc(1);if(y==x+1){}else{}`”. We call such programs “accidentally memory safe”.

Theorem 9 *Strong termination and strong memory safety remain undecidable in general, but strong memory safety of strongly terminating programs is decidable, assuming the oracle used for side conditions is a decision procedure.*

Proof. First two claims follow from the fact that SAFEKERNELC is Turing complete.

If a program is known to strongly terminate, it means its canonical derivation is finite, and by constructing it (which we can, given our oracle is a decision procedure) we can effectively check whether the last configuration obtained in this derivation is indeed final. \square

Theorem 9 gives us an effective procedure for runtime verification of strong memory safety. Given a closed program, one can simply “execute” it using the SAFEKERNELC definition. As long as the symbolic execution in SafeKernelC can proceed, strong memory safety is guaranteed to hold up to that point. That means that the execution of the program on a real machine using KernelC is guaranteed to be memory safe up to the same point. Ultimately, for terminating programs, this becomes a sound decision procedure for verifying memory safety.

5 Conclusions

We have presented the first (up to our knowledge) formal definition of memory safety for a language allowing direct allocation and addressing of memory. After showing that verification of memory safety is not amenable for automation in general, through a suite of undecidability results, we proposed strong memory safety, a meaningful restriction of memory safety, and proved that it is runtime verifiable. Our main result is that runtime verification of strong memory safety is a sound decision procedure for memory safety. The preliminary executable definition is available for download (and experimentation) as a part of the K-Maude distribution K-Maude [13].

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A KernelC Syntax and Configuration in K-Maude

```
(k syntax for KERNELC is including GENERIC-EXP-SYNTAX + STRING-SYNTAX .
  sorts Stmt StmtList Pgm .
  subsort Stmt < StmtList .
  op #include<stdio.h>#include<stdlib.h>#void'main'(void)['_']
    : StmtList -> Pgm [renameTo _] .
  op *_ : Exp -> Exp [strict prec 25] .
  op !_ : Exp -> Exp [aux] .
  vars E E' : Exp .
  eq ! E = E ? 0 : 1 .
  ops _&&_ _||_ : Exp Exp -> Exp [aux] .
  eq E && E' = E ? E' : 0 .
  eq E || E' = E ? 1 : E' .
  op _?_:_ : Exp Exp Exp -> Exp [renameTo if'(_')_else_ prec 39] .
  op _:=_ : Exp Exp -> Exp [strict(2) prec 40 gather (e E)] .
  op _; : Exp -> Stmt [prec 45 strict] .
  op _; : -> Stmt [renameTo .K] .
  op __ : StmtList StmtList -> StmtList [prec 100 gather(e E) renameTo _->_] .
  op '[_'] : StmtList -> Stmt [renameTo _] .
  op '{_}' : -> Stmt [renameTo .K] .
  op malloc'(_') : Exp -> Exp [strict] .
  op free'(_') : Exp -> Exp [strict] .
  op if'(_')_ : Exp Stmt -> Stmt [aux prec 47] .
  var St St' : Stmt .
  eq if(E) St = if (E) St else {} .
  op if'(_')_else_ : Exp Stmt Stmt -> Stmt [strict (1) prec 46] .
  op while'(_')_ : Exp Stmt -> Stmt .
  op printf'("%d "',_') : Exp -> Exp [strict] .
  op null : -> Exp [aux] .
  eq null = 0 .
```

k)

```
(k configuration for KERNELC is
  including KMAP{K, K} + FRESH-ITEM{K} .
  ops env mem ptr : -> CellLabel [wrapping Map'{K',K'}] .
  op out : -> CellLabel [wrapping K] .
  op stream : String -> K .
  op void : -> KResult .
```

k)

B KernelC Semantics in K-Maude

```

(k semantics for KERNELC is including GENERIC-EXP-SEMANTICS + INT-SIMP .
var P : Pgm . var N N' : Nat . var X : Name . var Env : Map{K,K} .
var V V' : KResult . var I : Int . var Ptr Mem : Map{K,K} .
var K K1 K2 : K . var S : String .
kcxt * K1 := K2 [strict(1)] . --- evaluating lhs to a lVal
eq <T> P </T> = <T> <k> mkK(P) </k> <env> .empty </env>
      <mem> .empty </mem> <ptr> .empty </ptr>
      <nextItem> item(1) </nextItem>
      <out> stream("") </out> </T> .
eq #(true) = #(1) . eq #(false) = #(0) .
ceq if #(I) K1 else K2 = K2 if I eq 0 .
ceq if #(I) K1 else K2 = K1 if I neq 0 .
eq V ; = .K . --- discarding the value of an expression statement
keq <k> [[X ==> V]] ...</k> <env>... X |-> V ...</env> .
keq <k> [[X := V ==> V]] ...</k> <env> [[Env ==> Env[X <- V]]] </env> .
keq <k> [[* #(N) ==> V]] ...</k> <mem>... #(N) |-> V ...</mem> .
keq <k> [[* #(N) := V ==> V]] ...</k>
      <mem>... #(N) |-> [[V' ==> V]] ...</mem> .
keq <k> [[while(K1) K2 ==> if(K1) (K2 -> while(K1) K2) else .K]] ...</k> .
op alloc : Nat Nat -> Map{K,K} .
eq alloc(N, 0) = .empty .
eq alloc(N, s(N')) = (#(N) |-> #(N + 1)) &' alloc(N + 1, N') .
keq <k> [[ malloc(#(N)) ==> #(var(N'))]] ...</k>
      <ptr>... [[.empty ==> #(var(N')) |-> #(N)]] ...</ptr>
      <nextItem> [[item(N') ==> item(N') + s(N)]] </nextItem>
      <mem>... [[.empty ==> alloc(var(N'), N)]] ...</mem> .
op freeMem : Map{K,K} Nat Nat -> Map{K,K} .
eq freeMem(Mem, N, 0) = Mem .
eq freeMem((Mem &' (#(N) |-> V)), N, s(N')) = freeMem(Mem,N + 1,N') .
keq <k> [[free(#(N)) ==> void]] ...</k>
      <ptr>... [[#(N) |-> #(N') ==> .empty]] ...</ptr>
      <mem> [[Mem ==> freeMem(Mem, N, N')]] </mem> .
keq <k> [[printf("%d ",#(I)) ==> void]] ...</k>
      <out> [[stream(S) ==> stream(S + string(I,10)+ " ")]] </out> .
k)

```

C SafeKernelC Semantics in K-Maude

Since it relies on the same syntax and configuration, it is called a semantics for KernelC as the previous definition. The only thing that changes is that the module NAT-VARS is now imported provide symbolic naturals instead of the simplification module INT-SIMP, and the semantic rule for malloc(including alloc).

```
(k semantics for KERNELC is including GENERIC-EXP-SEMANTICS + NAT-VARS .
  var P : Pgm . var N N' : Nat . var X : Name . var Env : Map{K,K} .
  var V V' : KResult . var I : Int . var Ptr Mem : Map{K,K} .
  var K K1 K2 : K . var S : String .
  kcxt * K1 := K2 [strict(1)] . --- evaluating lhs to a lVal
  eq <T> P </T> = <T> <k> mkK(P) </k> <env> .empty </env>
      <mem> .empty </mem> <ptr> .empty </ptr>
      <nextItem> item(1) </nextItem>
      <out> stream("") </out> </T> .
  eq #(true) = #(1) . eq #(false) = #(0) .
  ceq if #(I) K1 else K2 = K2 if I eq 0 .
  ceq if #(I) K1 else K2 = K1 if I neq 0 .
  eq V ; = .K . --- discarding the value of an expression statement
  keq <k> [[X ==> V]] ...</k> <env>... X |-> V ...</env> .
  keq <k> [[X := V ==> V]] ...</k> <env> [[Env ==> Env[X <- V]]] </env> .
  keq <k> [[* #(N) ==> V]] ...</k> <mem>... #(N) |-> V ...</mem> .
  keq <k> [[* #(N) := V ==> V]] ...</k>
      <mem>... #(N) |-> [[V' ==> V]] ...</mem> .
  keq <k> [[while(K1) K2 ==> if(K1) (K2 -> while(K1) K2) else .K]] ...</k> .
  op alloc : Nat Nat -> Map{K,K} .
  eq alloc(N, 0) = .empty .
  eq alloc(var(NV), s(N'))
    = (#(var(NV)) |-> #(var(NV + 1))) &' alloc(var(NV) + 1, N') .
  eq alloc(var(NV) + N, s(N'))
    = (#(var(NV) + N) |-> #(var(NV + N + 1))) &' alloc(var(NV) + N + 1, N') .
  keq <k> [[ malloc(#(N)) ==> #(var(N'))]] ...</k>
      <ptr>... [[.empty ==> (#(var(N')) |-> #(N))]] ...</ptr>
      <nextItem> [[item(N') ==> item(N') + s(N)]] </nextItem>
      <mem>... [[.empty ==> alloc(var(N'), N)]] ...</mem> .
  op freeMem : Map{K,K} Nat Nat -> Map{K,K} .
  eq freeMem(Mem, N, 0) = Mem .
  eq freeMem((Mem &' (#(N) |-> V)), N, s(N')) = freeMem(Mem, N + 1, N') .
  keq <k> [[free(#(N)) ==> void]] ...</k>
      <ptr>... [[#(N) |-> #(N') ==> .empty]] ...</ptr>
      <mem> [[Mem ==> freeMem(Mem, N, N')]] </mem> .
  keq <k> [[printf("%d ", #(I)) ==> void]] ...</k>
      <out> [[stream(S) ==> stream(S + string(I,10)+ " ")]] </out> .
```

k)